

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"6466615".pn.	US-PGPUB; USPAT	OR	ON	2005/02/16 11:32
L2	1	"375"/\$.ccls. and ((synchronization adj circuit) with (dummy adj buffer))	US-PGPUB; USPAT	OR	ON	2005/02/16 11:33
L3	0	"327"/\$.ccls. and ((synchronization adj circuit) with (dummy adj buffer))	US-PGPUB; USPAT	OR	ON	2005/02/16 11:33
L4	0	"370"/\$.ccls. and ((synchronization adj circuit) with (dummy adj buffer))	US-PGPUB; USPAT	OR	ON	2005/02/16 11:33
L5	0	"713"/\$.ccls. and ((synchronization adj circuit) with (dummy adj buffer))	US-PGPUB; USPAT	OR	ON	2005/02/16 11:33
L6	4509	synchronization adj circuit	US-PGPUB; USPAT	OR	ON	2005/02/16 13:30
L7	188	L6 and (delay adj locked adj loop)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:30
L8	2	ton.xp. and (delay\$5 adj locked adj loop) and (dummy adj load)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:31
L9	20	"327"/\$.ccls. and (delay adj locked adj loop) and (synchronization adj circuit) and Offset	US-PGPUB; USPAT	OR	ON	2005/02/16 13:34
L10	0	"327"/\$.ccls. and (delay adj locked adj loop) and (synchronization adj circuit) and (Offset adj input)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:34
L11	0	"375"/\$.ccls. and (delay adj locked adj loop) and (synchronization adj circuit) and (Offset adj input)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:34
L12	0	"375"/\$.ccls. and (delay adj locked adj loop) and (synchronization adj circuit) and (Offset near input\$5)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:34
L13	0	"713"/\$.ccls. and (delay adj locked adj loop) and (synchronization adj circuit) and (Offset near input\$5)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:35
L14	27	"713"/\$.ccls. and (delay adj locked adj loop) and (synchronization) and (Offset)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:38
L15	1	"713"/\$.ccls. and (dummy adj load) and (synchronization) and (Offset)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:39
L16	0	"713"/\$.ccls. and (dummy adj buffer) and (synchronization) and (Offset)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:39
L17	7	"327"/\$.ccls. and (dummy adj load) and (synchronization) and (Offset)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:40

L18	9	"375"/\$.ccls. and (dummy adj load) and (synchronization) and (Offset)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:40
S1	196	327/144.ccls. and synchroniz\$5	US-PGPUB; USPAT	OR	ON	2005/01/11 15:51
S2	86	327/152.ccls. and synchroniz\$5	US-PGPUB; USPAT	OR	ON	2004/11/05 16:07
S3	7	327/152.ccls. and synchroniz\$5 and DLL	US-PGPUB; USPAT	OR	ON	2004/11/05 16:01
S4	52	"327"/\$.ccls. and (synchronizing adj circuit) and select\$5 and DLL	US-PGPUB; USPAT	OR	ON	2004/11/05 16:14
S5	17	"327"/\$.ccls. and (synchronizing adj circuit) and (selection adj circuit) and DLL	US-PGPUB; USPAT	OR	ON	2004/12/02 12:08
S6	1	"5790952".pn.	US-PGPUB; USPAT	OR	ON	2004/11/05 17:28
S7	81	"327"/\$.ccls. and (selection adj circuit) and DLL	US-PGPUB; USPAT	OR	ON	2004/12/02 12:09
S8	37	375/373.ccls. and (delay\$2 adj locked\$3 adj loop)	US-PGPUB; USPAT	OR	ON	2005/01/11 16:12
S9	1	375/373.ccls. and (delay\$2 adj locked\$3 adj loop) and retim\$5	US-PGPUB; USPAT	OR	ON	2005/01/11 16:12
S10	1	375/355.ccls. and (delay\$2 adj locked\$3 adj loop) and retim\$5	US-PGPUB; USPAT	OR	ON	2005/01/11 16:13
S11	1	375/355.ccls. and (delay\$2 adj locked\$3 adj loop) and retim\$5	US-PGPUB; USPAT	OR	ON	2005/01/11 16:16
S12	27	375/355.ccls. and retim\$5	US-PGPUB; USPAT	OR	ON	2005/01/11 16:14
S13	1	375/373.ccls. and (delay\$2 adj locked\$3 adj loop) and retim\$5	US-PGPUB; USPAT	OR	ON	2005/01/11 16:16
S14	9	375/376.ccls. and (delay\$2 adj locked\$3 adj loop) and retim\$5	US-PGPUB; USPAT	OR	ON	2005/01/11 16:18
S15	0	327/144.ccls. and (delay\$2 adj locked\$3 adj loop) and retim\$5	US-PGPUB; USPAT	OR	ON	2005/01/11 16:18
S16	12	327/144.ccls. and retim\$5	US-PGPUB; USPAT	OR	ON	2005/01/11 16:20
S17	3	327/144.ccls. and (synchronizat\$5) same retim\$5	US-PGPUB; USPAT	OR	ON	2005/01/11 16:21
S18	2	327/144.ccls. and (synchronizat\$5) and retim\$5 and selection	US-PGPUB; USPAT	OR	ON	2005/01/11 16:22
S19	0	327/144.ccls. and (synchronizat\$5) and retim\$5 and selection and (delay\$5 adj lock\$5)	US-PGPUB; USPAT	OR	ON	2005/01/11 16:23
S20	5	"327"/\$.ccls. and (synchronizat\$5) and retim\$5 and selection and (delay\$5 adj lock\$5)	US-PGPUB; USPAT	OR	ON	2005/01/11 16:23

S21	55	"327"/\$.ccls. and (synchronizat\$5) and (selection adj circuit) and (delay\$5 adj lock\$5)	US-PGPUB; USPAT	OR	ON	2005/01/11 16:24
S22	0	327/162.ccls. and (DLL) and (dummy adj load)	US-PGPUB; USPAT	OR	ON	2005/01/12 14:32
S23	2	327/162.ccls. and (DLL) and (synchronization adj circuit)	US-PGPUB; USPAT	OR	ON	2005/01/12 14:33
S24	18	"327"/\$.ccls. and (DLL) and (synchronization adj circuit) and Offset	US-PGPUB; USPAT	OR	ON	2005/02/16 13:32
S25	4	"327"/\$.ccls. and (DLL) and (synchronization adj circuit) and (Offset adj select\$5)	US-PGPUB; USPAT	OR	ON	2005/01/12 14:45
S26	0	"327"/\$.ccls. and (DLL) and (synchronization adj circuit) and (Offset adj select\$5 adj circuit)	US-PGPUB; USPAT	OR	ON	2005/01/12 14:52
S27	1	(DLL) and (synchronization adj circuit) and (Offset adj select\$5 adj circuit)	US-PGPUB; USPAT	OR	ON	2005/01/12 14:47
S28	1	"6275525".pn.	US-PGPUB; USPAT	OR	ON	2005/01/12 14:47
S29	1	"6275555".pn.	US-PGPUB; USPAT	OR	ON	2005/01/12 14:51
S30	1	"6466615".pn.	US-PGPUB; USPAT	OR	ON	2005/01/12 14:51
S31	0	"713"/503.ccls. and (DLL) and (synchronization adj circuit) and (Offset adj select)	US-PGPUB; USPAT	OR	ON	2005/01/12 14:52
S32	6	"713"/503.ccls. and (DLL) and (synchronization adj circuit)	US-PGPUB; USPAT	OR	ON	2005/01/12 14:59
S33	5	"713"/500.ccls. and (DLL) and (synchronization adj circuit)	US-PGPUB; USPAT	OR	ON	2005/01/12 14:57
S34	0	"327"/\$.ccls. and (DLL) and (dummmmy adj load)	US-PGPUB; USPAT	OR	ON	2005/01/12 15:27
S35	0	adaptive adj deskew\$5 adj clock	US-PGPUB; USPAT	OR	ON	2005/01/12 15:28
S36	2	adaptive adj de adj skew\$5 adj clock	US-PGPUB; USPAT	OR	ON	2005/01/12 15:32
S37	0	375/354.ccls. and (Delay\$5 adj lock\$3 adj loop) and (dummy adj load) and (offset adj value)	US-PGPUB; USPAT	OR	ON	2005/01/12 15:33
S38	1	375/354.ccls. and (Delay\$5 adj lock\$3 adj loop) and (dummy adj buffer) and (offset adj value)	US-PGPUB; USPAT	OR	ON	2005/01/12 15:34
S39	3	375/354.ccls. and (Delay\$5 adj lock\$3 adj loop) and (offset adj value)	US-PGPUB; USPAT	OR	ON	2005/01/12 15:35

S40	5	375/371.ccls. and (Delay\$5 adj lock\$3 adj loop) and (offset adj value)	US-PGPUB; USPAT	OR	ON	2005/01/12 15:36
S41	2	375/373.ccls. and (Delay\$5 adj lock\$3 adj loop) and (offset adj value)	US-PGPUB; USPAT	OR	ON	2005/01/12 15:36
S42	10	375/376.ccls. and (Delay\$5 adj lock\$3 adj loop) and (offset adj value)	US-PGPUB; USPAT	OR	ON	2005/02/14 09:42
S43	390	bocure.xa.	US-PGPUB; USPAT	OR	ON	2005/02/12 11:10
S44	0	bocure.xa. and (delay\$5 adj lock\$5 adj loop)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:30
S45	0	bocure.xa. and (delay\$5 adj clock\$5 adj loop)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:11
S46	259	bocure.xa. and (delay\$5)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:12
S47	14	bocure.xa. and (clock adj delay\$5)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:14
S48	6	bocure.xa. and (data with retim\$5)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:23
S49	4	vo.xa. and (data with retim\$5)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:19
S50	9	vo.xp. and (data with retim\$5)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:19
S51	5	("4734900" "5132957" "5438303" "5640131" "6028898").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/12 11:19
S52	5	bocure.xp. and (data with retim\$5)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:24
S53	5	ton.xp. and (data with retim\$5)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:25
S54	48	ton.xp. and (delay\$5 adj locked adj loop)	US-PGPUB; USPAT	OR	ON	2005/02/16 13:31
S55	22	bocure.xp. and (delay\$5 adj locked adj loop)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:31
S56	0	bocure.xp. and (delay\$5 adj locked adj loop) and (reti\$5 near data)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:31
S57	0	bocure.xp. and (delay\$5 adj locked adj loop) and (retim5 near data)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:31
S58	0	(delay\$5 adj locked adj loop) and (retim5 near data)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:31
S59	2793	(delay\$5 adj locked adj loop)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:31
S60	993	(delay\$5 adj locked adj loop) and (multiplexer)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:32

S61	405	(delay\$5 adj locked adj loop) and (multiplexer) and offset	US-PGPUB; USPAT	OR	ON	2005/02/12 11:32
S62	13	(delay\$5 adj locked adj loop) and (multiplexer) and offset and (data with retim\$5)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:41
S63	240	(delay\$5 adj locked adj loop) and (multiplexer) and (offset same (data))	US-PGPUB; USPAT	OR	ON	2005/02/12 11:42
S64	15	(delay\$5 adj locked adj loop) and (multiplexer) and ((offset adj value) same (data))	US-PGPUB; USPAT	OR	ON	2005/02/12 11:45
S65	37	(delay\$5 adj locked adj loop) and (multiplexer) and ((offset adj value) and (data))	US-PGPUB; USPAT	OR	ON	2005/02/12 11:48
S66	272	(reference adj clock) and (multiplexer) and ((offset adj value) and (data))	US-PGPUB; USPAT	OR	ON	2005/02/12 12:21
S67	4506	synchronization adj circuit	US-PGPUB; USPAT	OR	ON	2005/02/12 11:53
S68	214	S67 and DLL	US-PGPUB; USPAT	OR	ON	2005/02/16 13:29
S69	14	S68 and (offset adj value)	US-PGPUB; USPAT	OR	ON	2005/02/12 11:52
S70	35	370/516.ccls. and (synchronization adj circuit)	US-PGPUB; USPAT	OR	ON	2005/02/14 09:40
S71	10	("20020172311" "5081654" "5533072" "5587709" "5651033" "5675584" "5714904" "6185693" "6348826" "6441659").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/12 11:55
S72	1	370/516.ccls. and (synchronization adj circuit) and (delay\$5 adj lock\$4 adj loop)	US-PGPUB; USPAT	OR	ON	2005/02/12 12:02
S73	0	370/516.ccls. and (synchronization adj circuit) and (delay\$5 adj lock\$4 adj loop) and (offset adj value)	US-PGPUB; USPAT	OR	ON	2005/02/12 12:02
S74	0	"370"/\$.ccls. and (synchronization adj circuit) and (delay\$5 adj lock\$4 adj loop) and (offset adj value)	US-PGPUB; USPAT	OR	ON	2005/02/12 12:02
S75	0	"370"/520.ccls. and (delay\$5 adj lock\$4 adj loop) and (offset adj value)	US-PGPUB; USPAT	OR	ON	2005/02/12 12:03
S76	0	"370"/520.ccls. and (delay\$5 adj lock\$4 adj loop)	US-PGPUB; USPAT	OR	ON	2005/02/12 12:03
S77	9	"370"/517.ccls. and (delay\$5 adj lock\$4 adj loop)	US-PGPUB; USPAT	OR	ON	2005/02/12 12:03

S78	3	714/700.ccls. and (reference adj clock) and (multiplexer) and ((offset adj value) and (data))	US-PGPUB; USPAT	OR	ON	2005/02/12 12:47
S79	3	714/700.ccls. and (retim\$5 near data)	US-PGPUB; USPAT	OR	ON	2005/02/12 12:48
S80	43	(retim\$5 near data) and (delay\$5 adj locked adj loop)	US-PGPUB; USPAT	OR	ON	2005/02/12 12:49
S81	0	"327"/\$.ccls. and ((synchronization adj circuit) with (dummy adj buffer))	US-PGPUB; USPAT	OR	ON	2005/02/14 09:41
S82	1	((synchronization adj circuit) with (dummy adj buffer))	US-PGPUB; USPAT	OR	ON	2005/02/16 11:33
S83	1	((synchronization adj circuit) same (dummy adj buffer))	US-PGPUB; USPAT	OR	ON	2005/02/14 09:41
S84	0	((synchronization adj circuit) same (simulation adj load))	US-PGPUB; USPAT	OR	ON	2005/02/14 09:41
S85	1	((synchronization adj circuit) same (dummy adj load))	US-PGPUB; USPAT	OR	ON	2005/02/14 09:41
S86	0	(Delay\$5 adj lock\$3 adj loop) and (offset adj value) and (dummy adj load)	US-PGPUB; USPAT	OR	ON	2005/02/14 09:42
S87	68	(Delay\$5 adj lock\$3 adj loop) and (dummy adj load)	US-PGPUB; USPAT	OR	ON	2005/02/14 09:44
S88	5	(Delay\$5 adj lock\$3 adj loop) same (dummy adj load)	US-PGPUB; USPAT	OR	ON	2005/02/14 09:44
S89	0	(Delay\$5 adj lock\$3 adj loop) and (dummy adj load) and (retim\$5 near data)	US-PGPUB; USPAT	OR	ON	2005/02/14 09:44